

Fig. 3

Title: PROCESS FOR MANUFACTURING INTEGRATED DEVICES HAVING CONNECTIONS
ON A SEPARATE WAFER, AND INTEGRATED DEVICE THUS OBTAINED

Inventor(s): Ubaldo Mastromatteo

Express Mail No. EL755727707US

Docket No. 854063.665

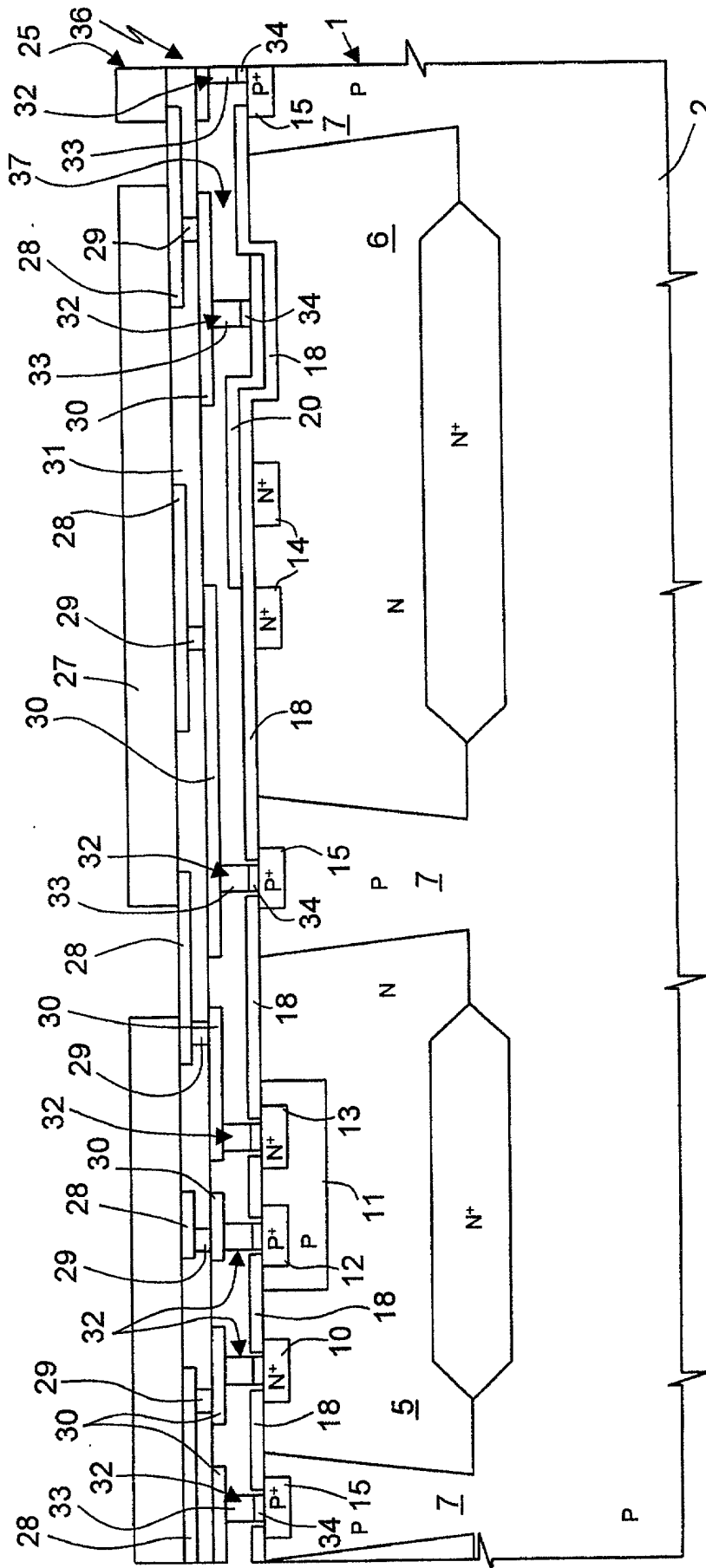


Fig. 4

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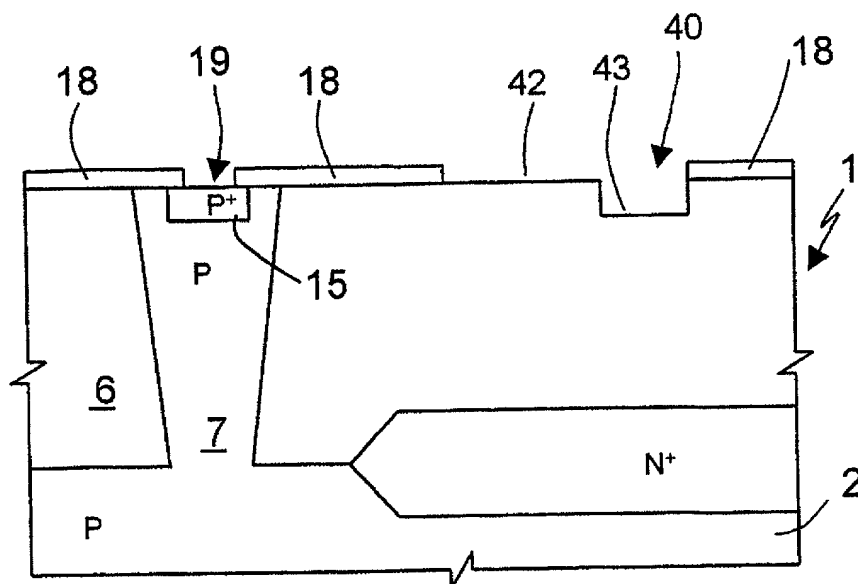


Fig. 5

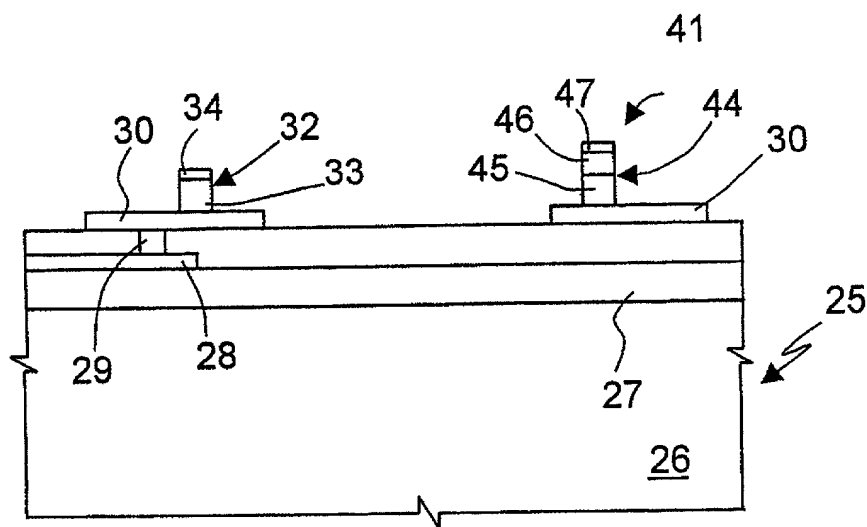


Fig. 6

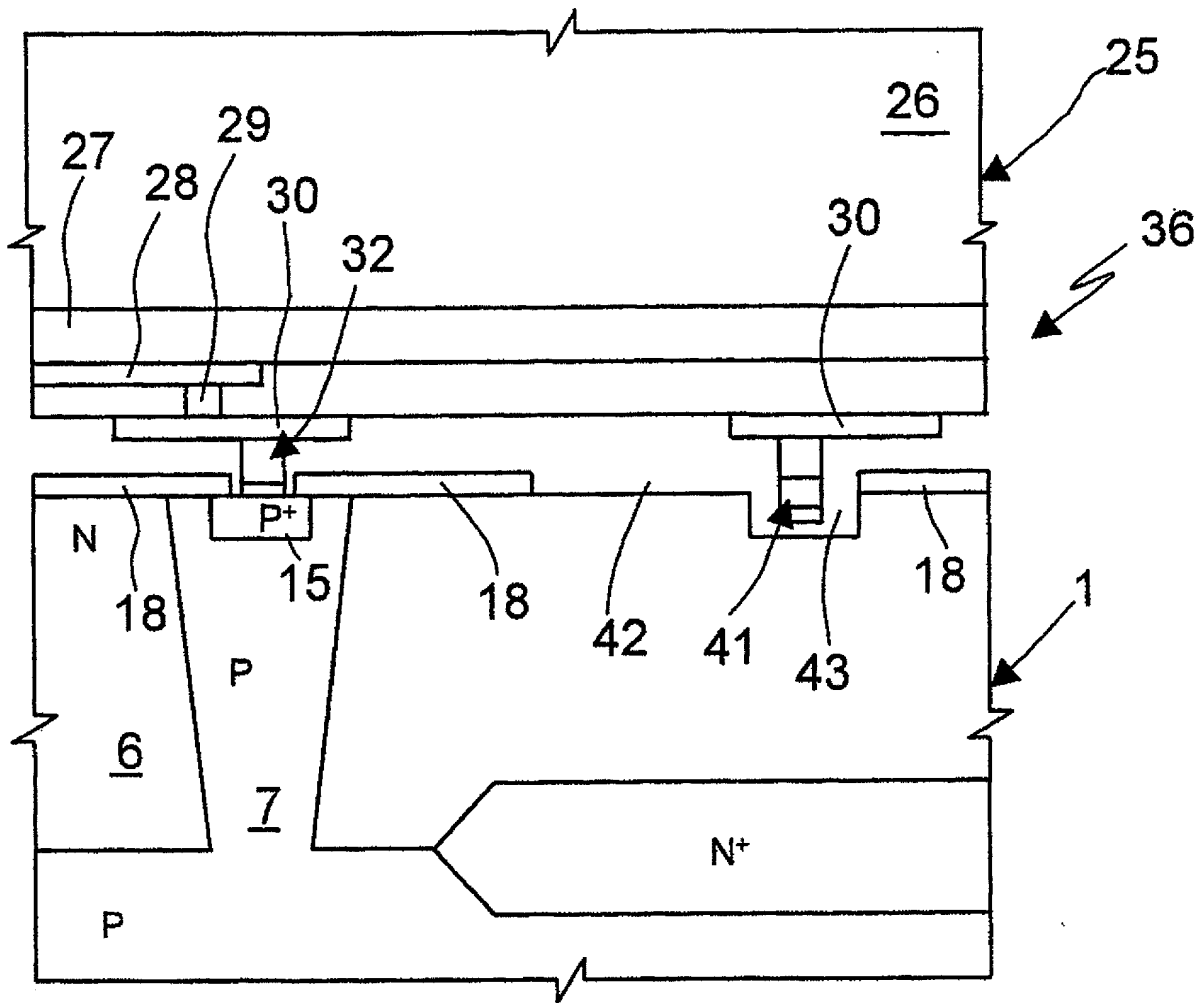


Fig. 7

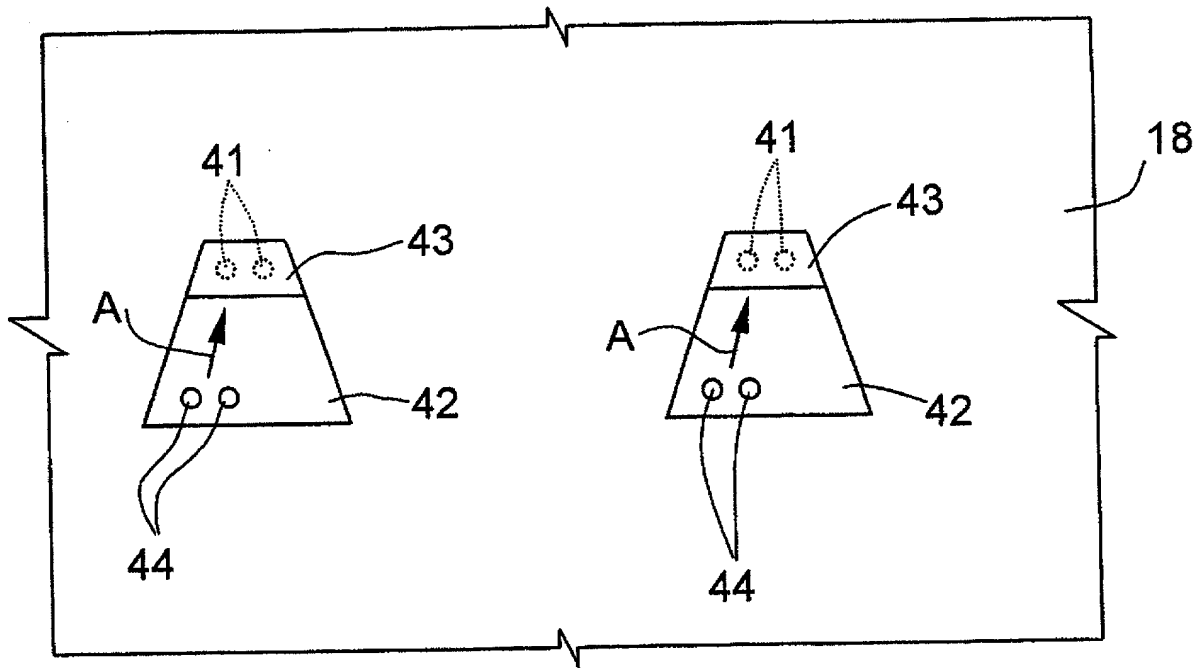


Fig. 8

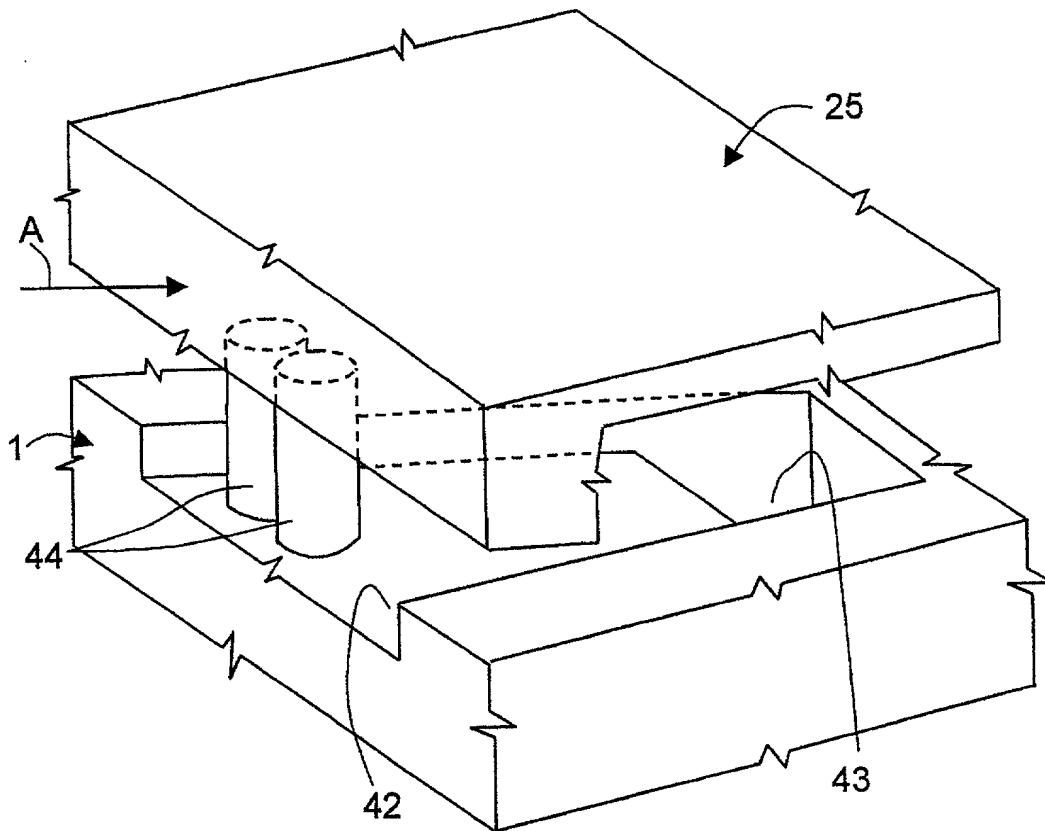


Fig. 9

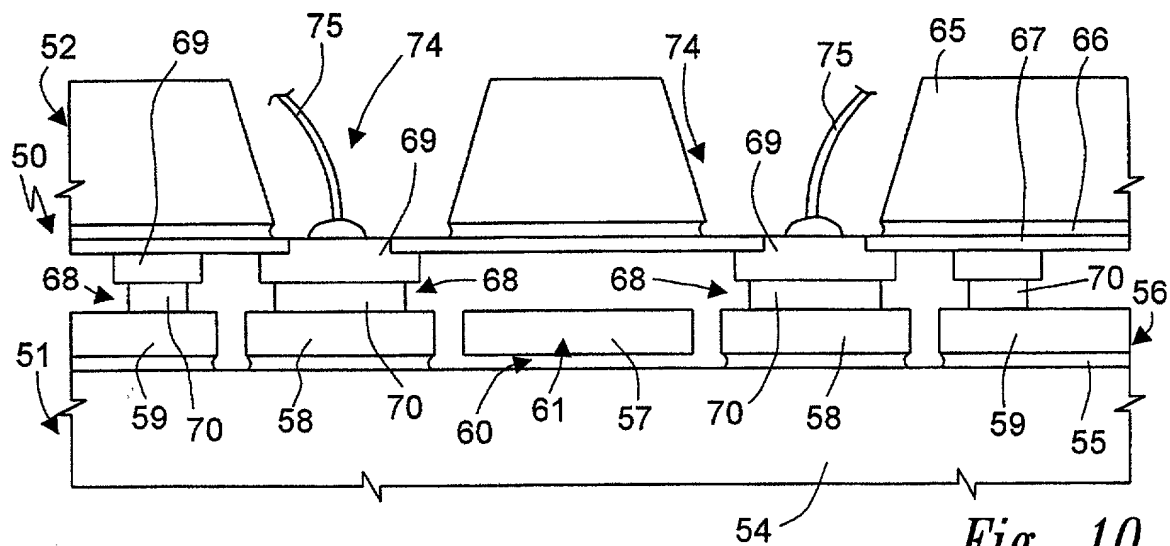


Fig. 10

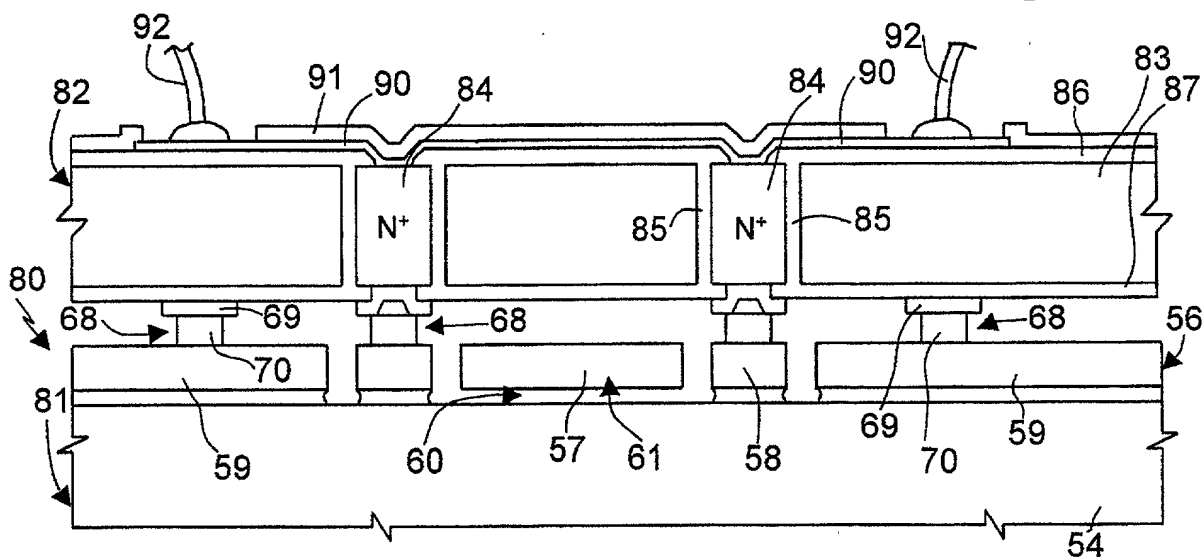


Fig. 11

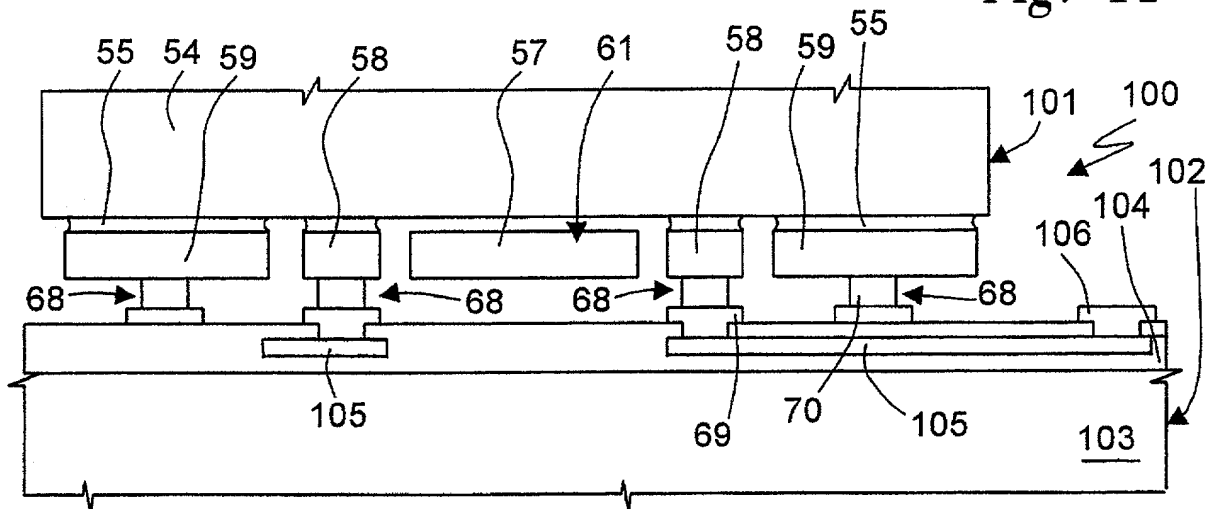


Fig. 12

